

FIG. 1 is a block diagram of a computer system 10 in accordance with the present invention. The computer system 10 includes a central processing unit (CPU) 16, a memory controller 18, a memory 12, an I/O controller 14, a network interface 26, an input device 20, a CRT display 22, and a projector 24. The CPU 16 and memory controller 18 are connected to the memory 12, I/O controller 14, and network interface 26. The input device 20 is connected to the I/O controller 14. The CRT display 22 and projector 24 are connected to the I/O controller 14.

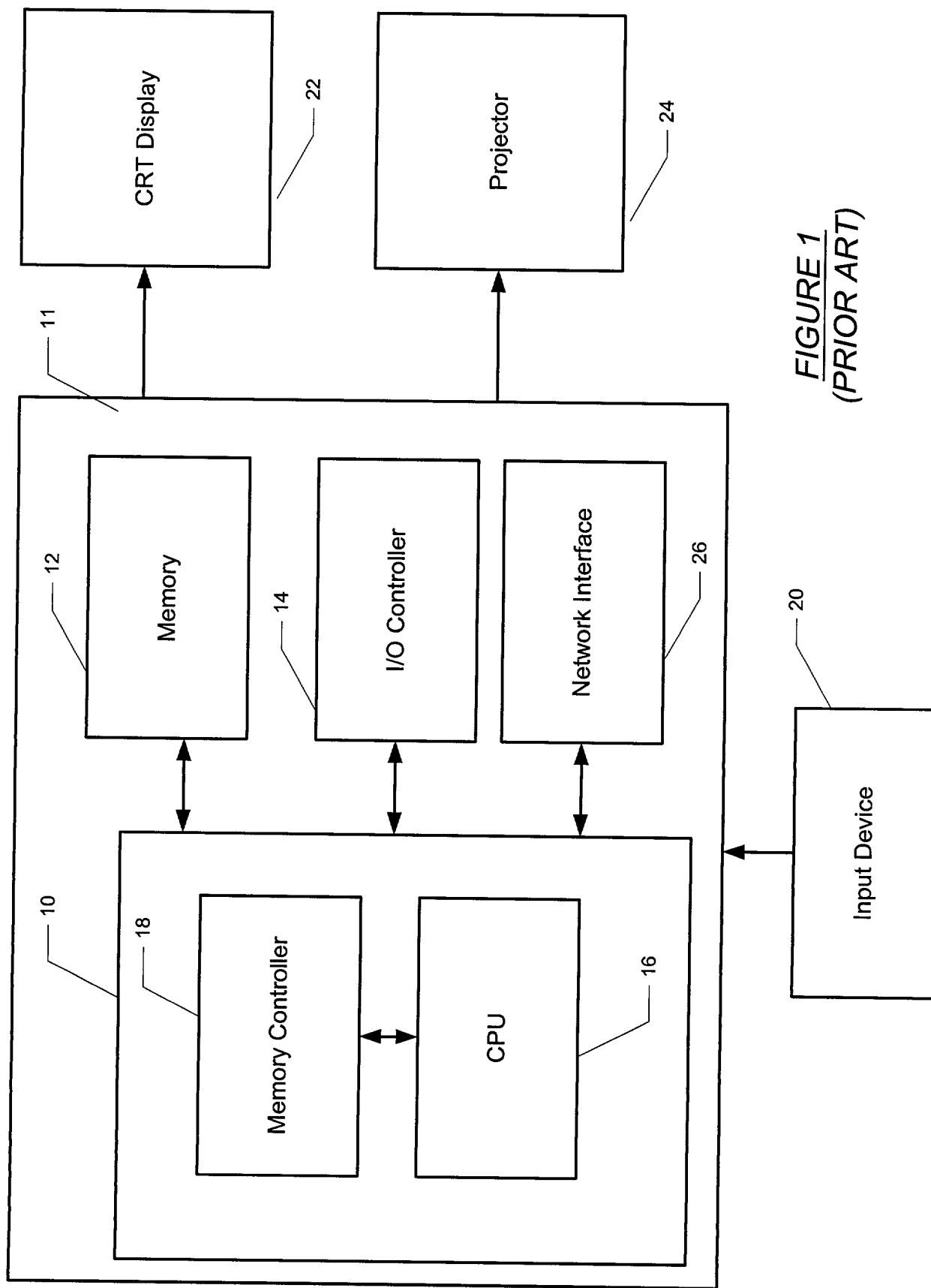


FIGURE 1
(PRIOR ART)

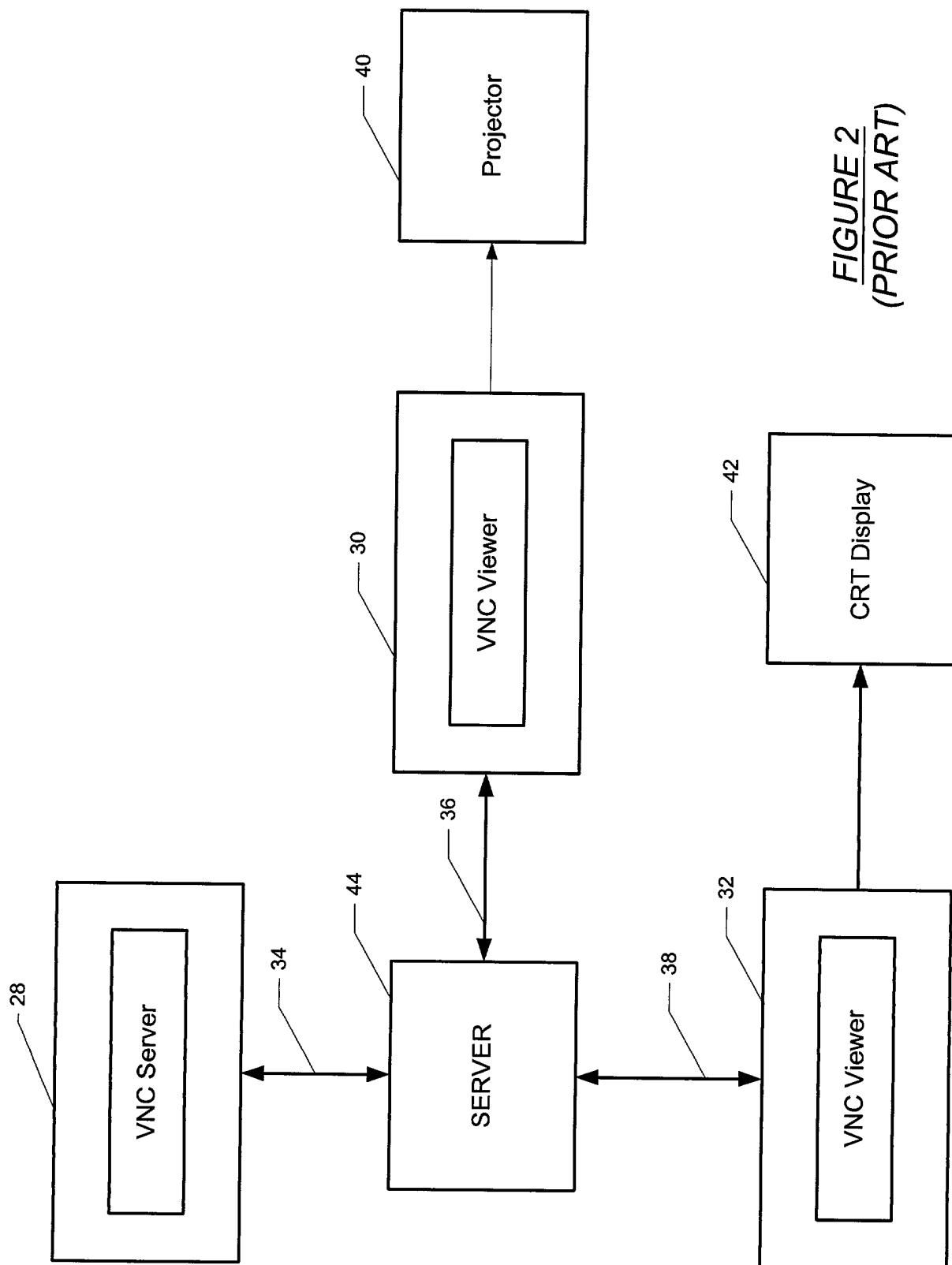


FIGURE 2
(PRIOR ART)

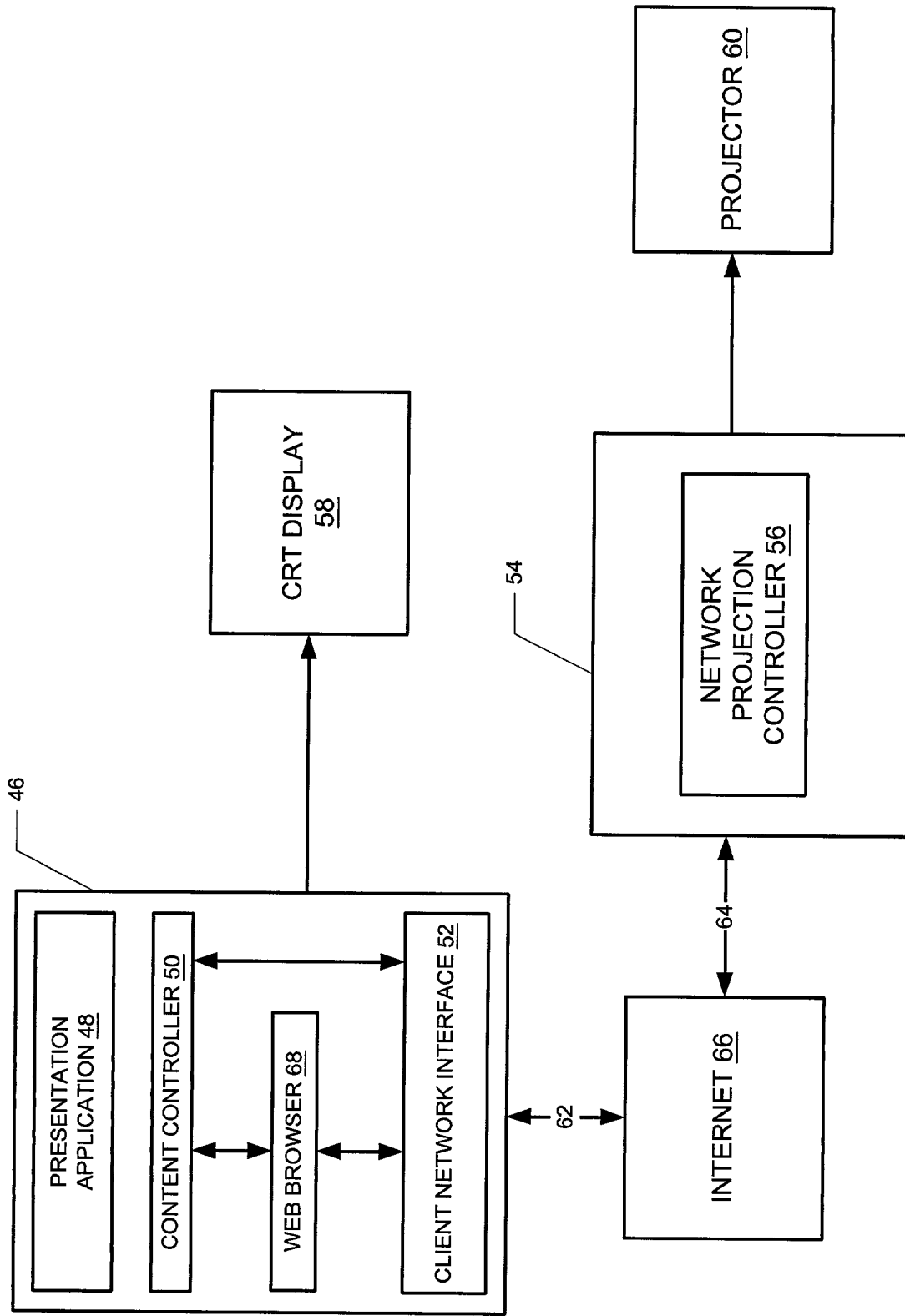


FIGURE 3

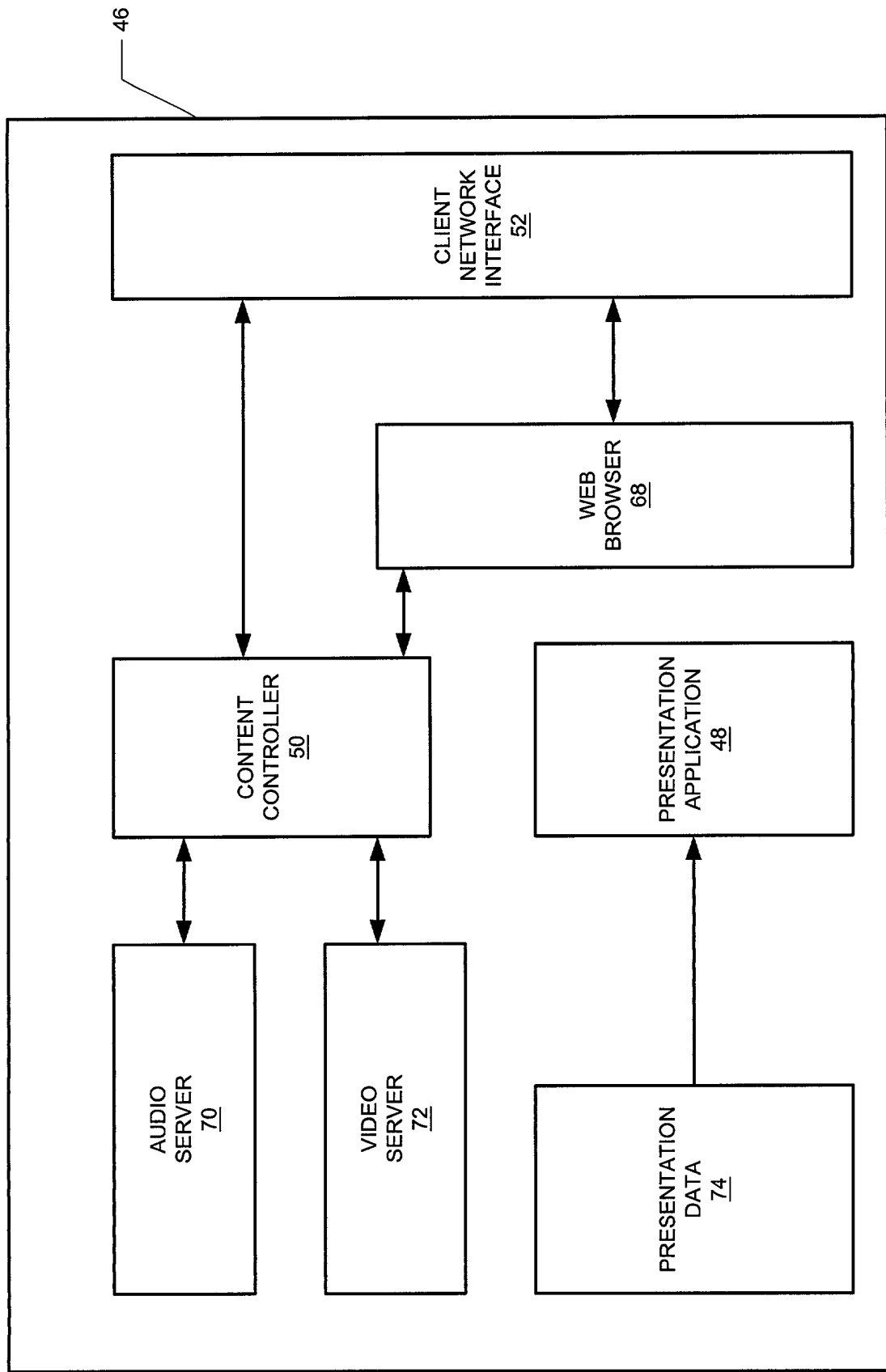


FIGURE 4

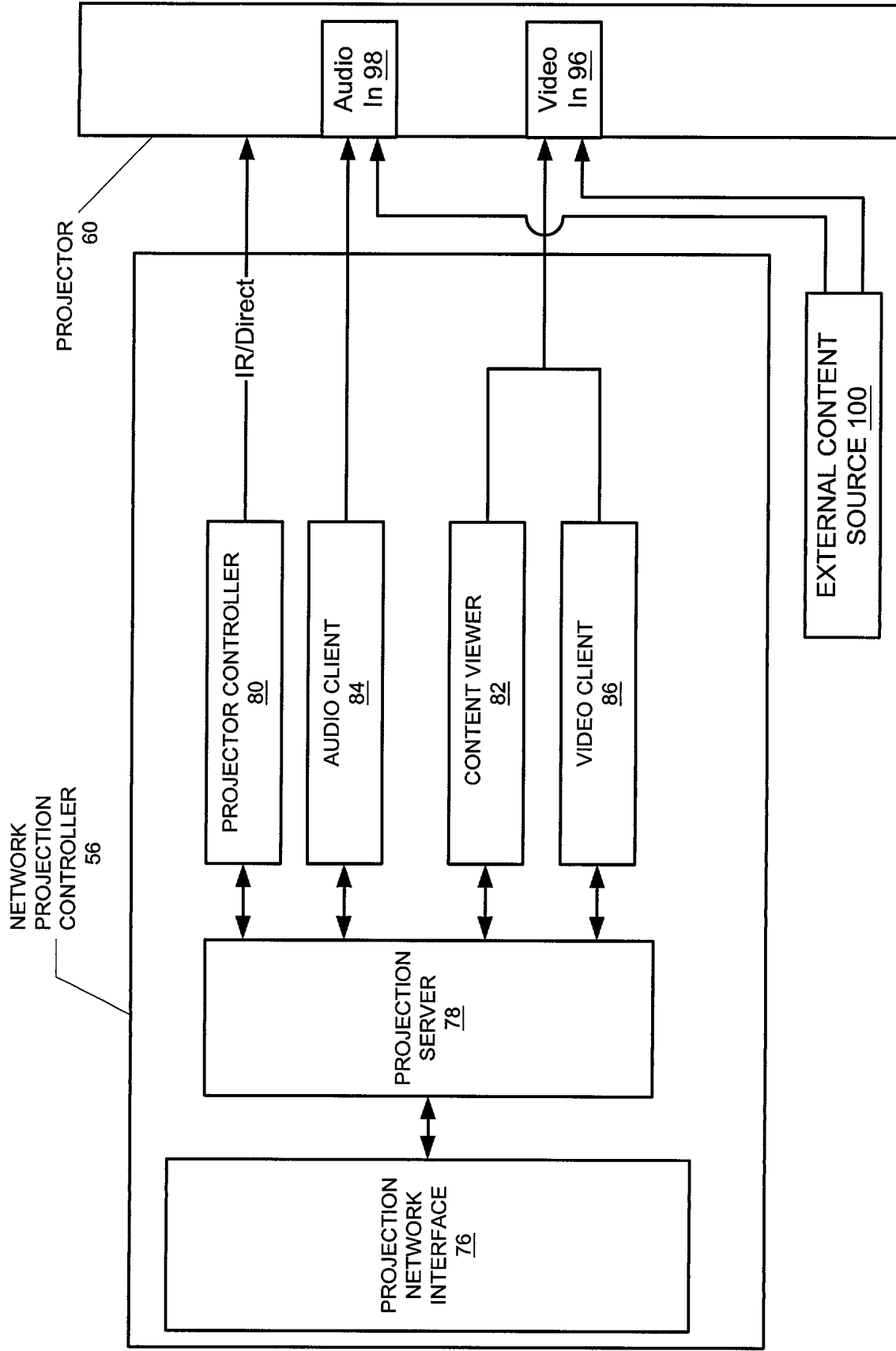


FIGURE 5

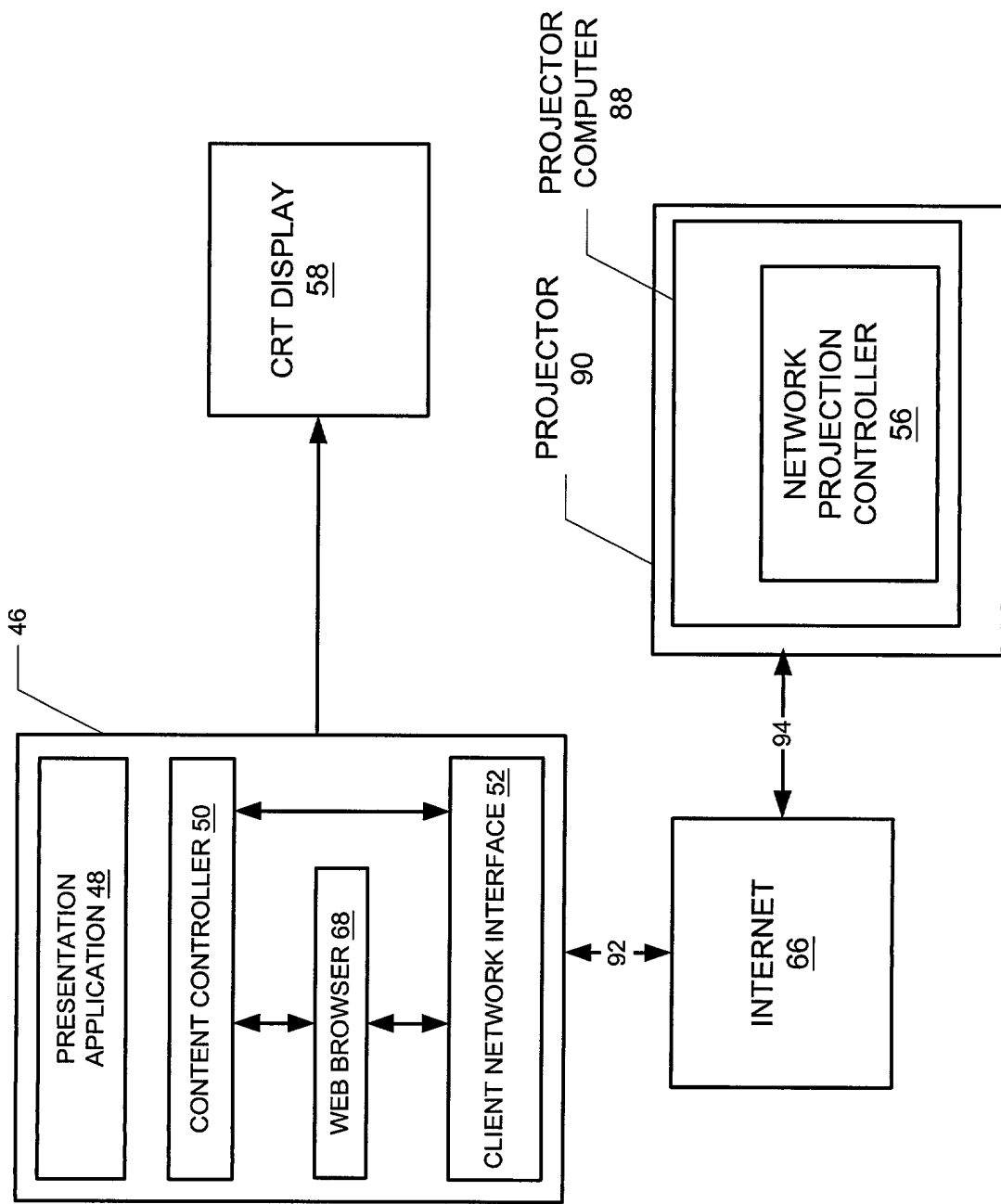


FIGURE 6

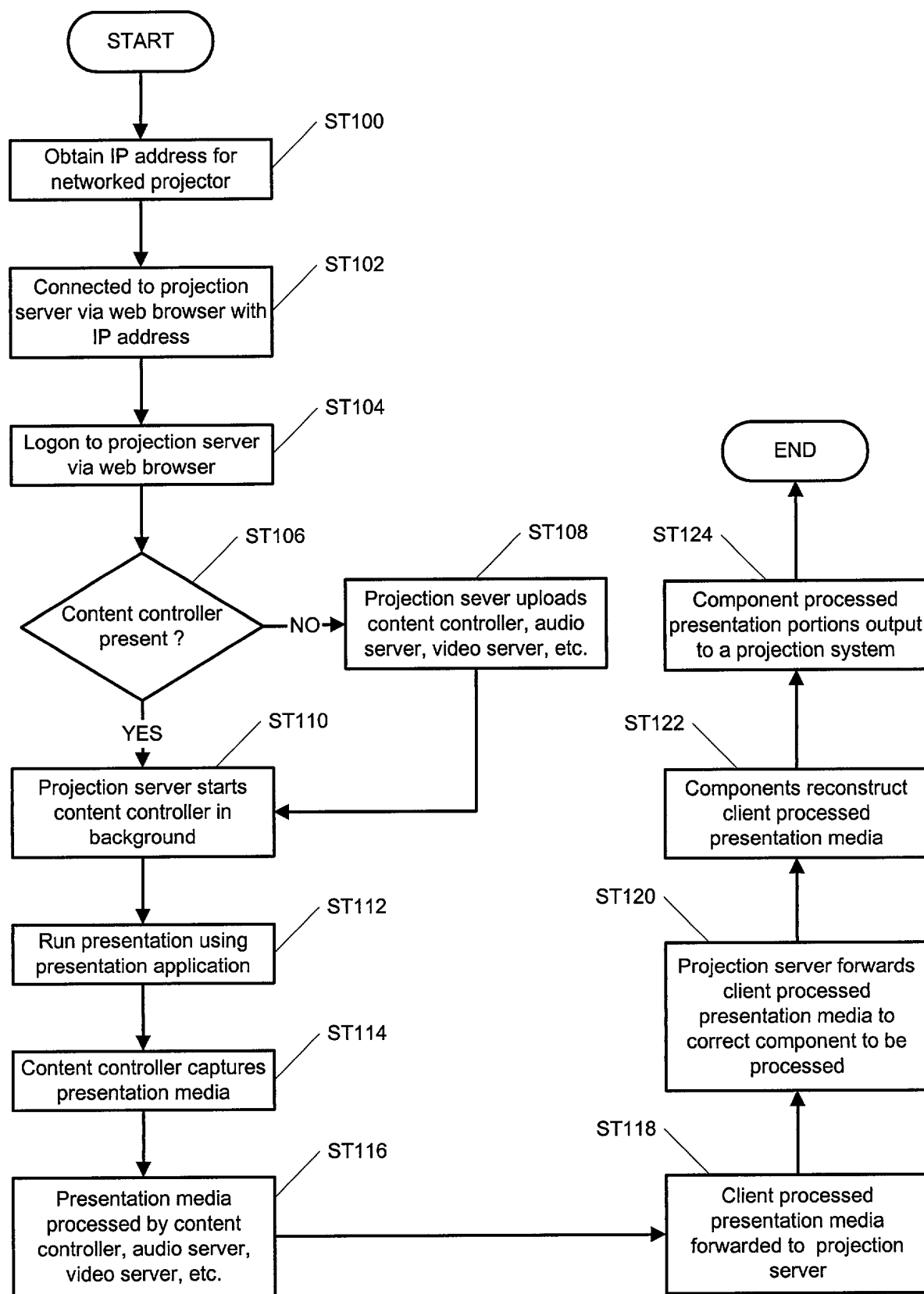


FIGURE 7